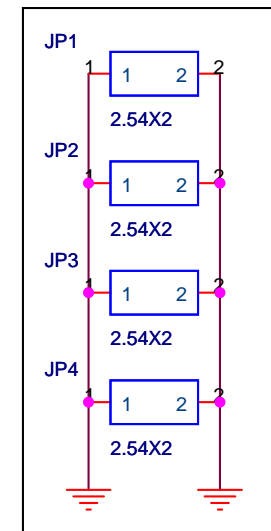
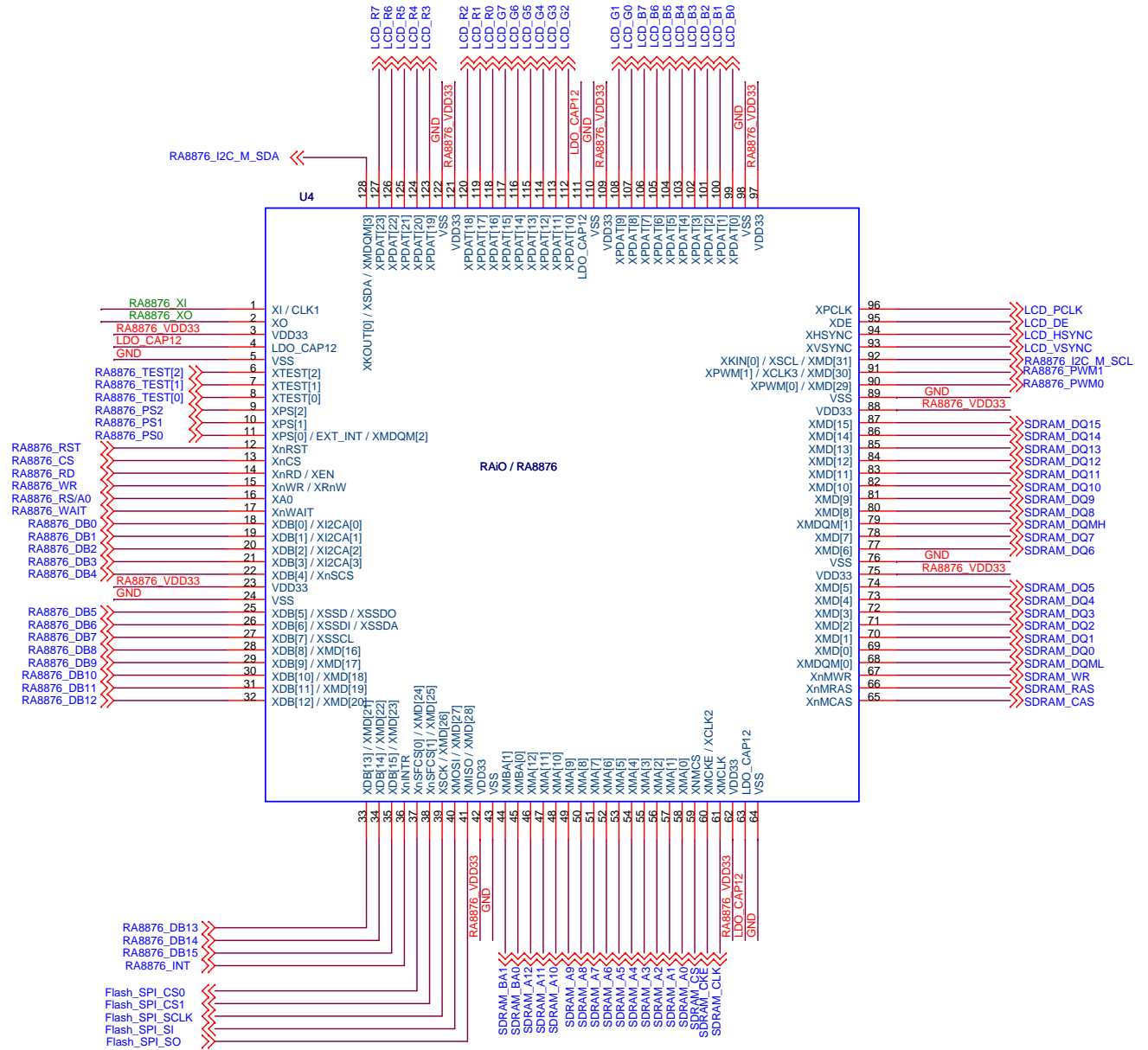
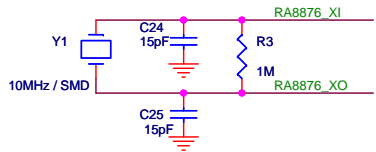
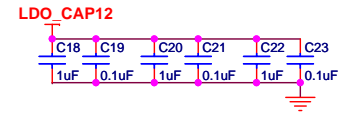
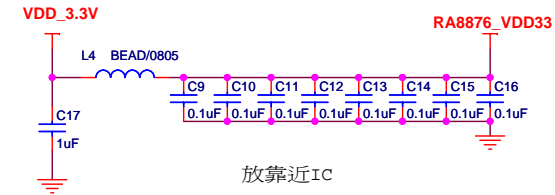


分布板子四邊



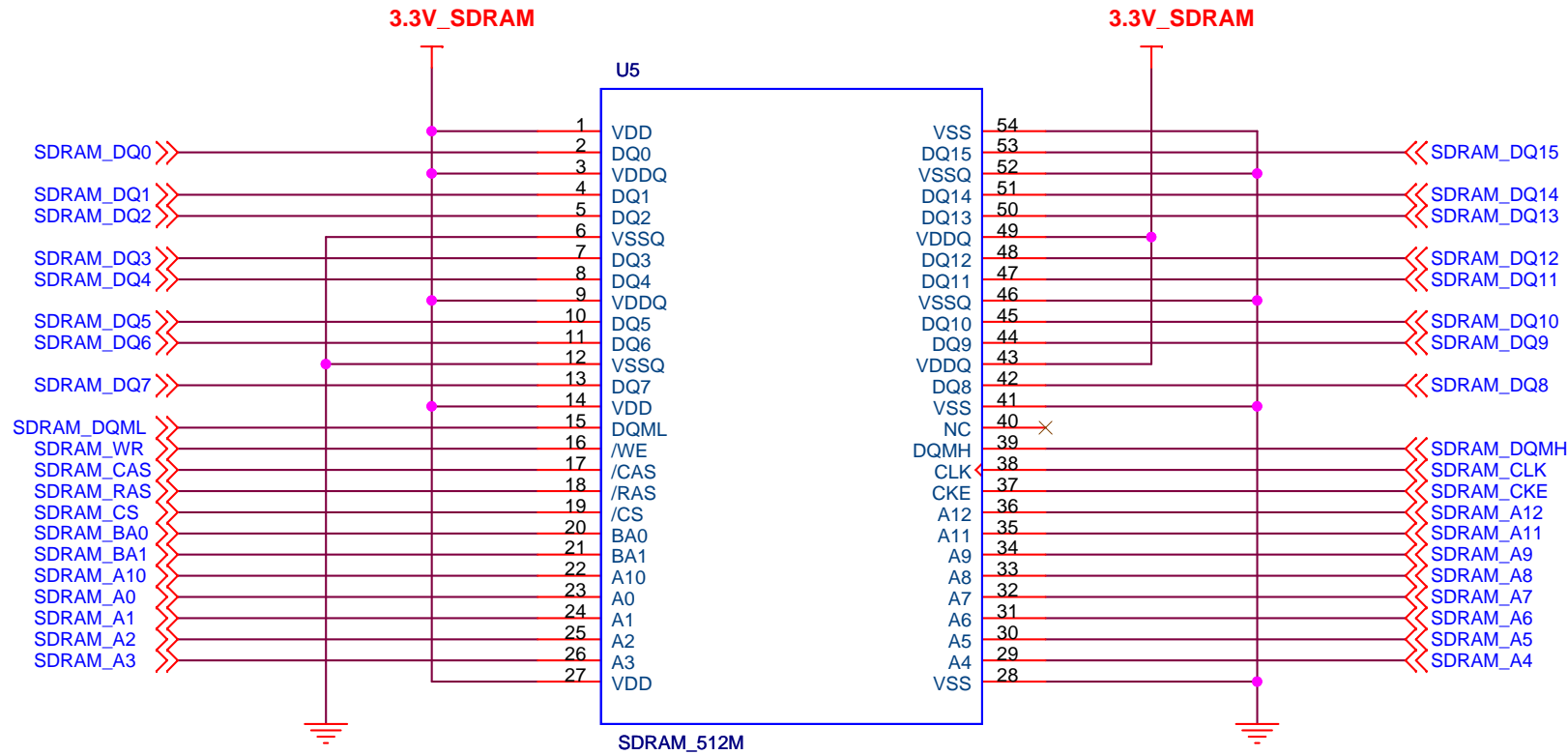
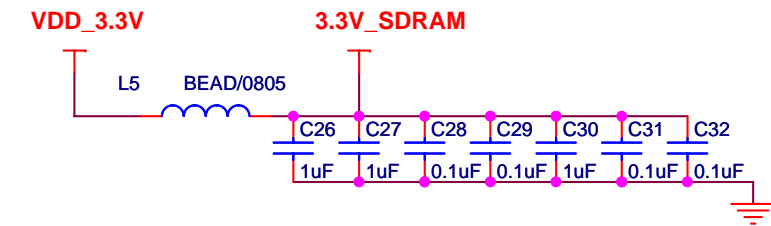
RA8876_B_Demo Board

Size A	Document Number 01_RA8876_VCC	Rev A
Date: Thursday, August 29, 2013	Sheet 1 of 10	



RA8876_B_Demo Board

Size B	Document Number	Rev A
	02_RA8876_Chip	
Date:	Thursday, August 29, 2013	Sheet 2 of 10



RA8876_B_Demo Board

Size
A

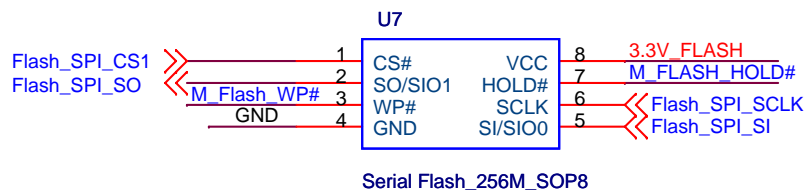
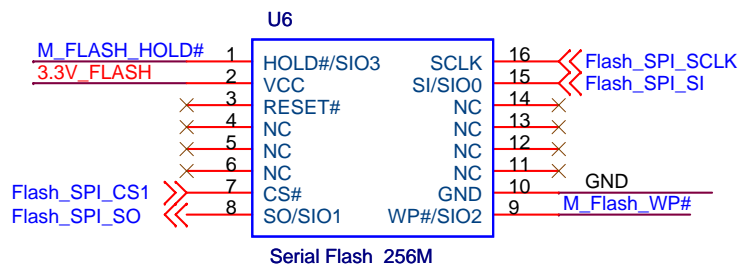
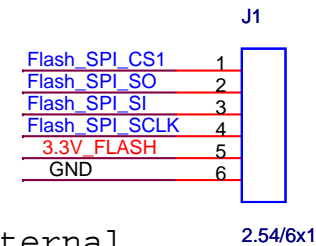
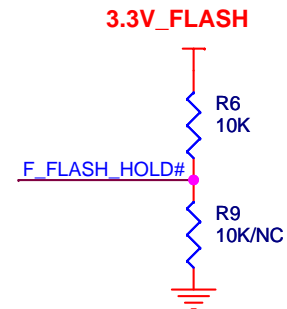
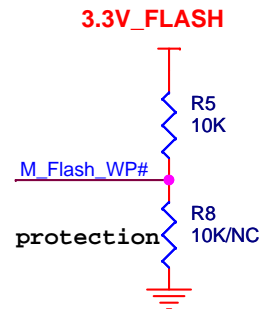
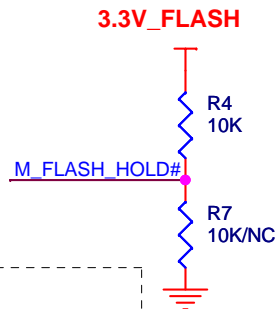
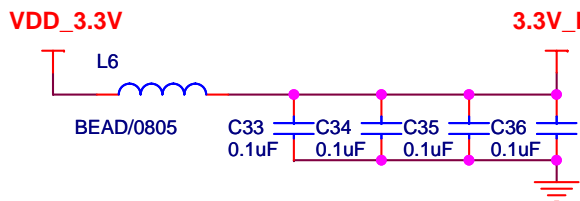
Document Number

03_RA8876_SDRAM

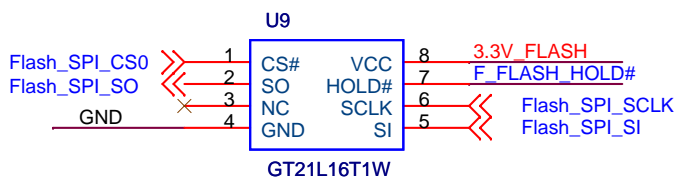
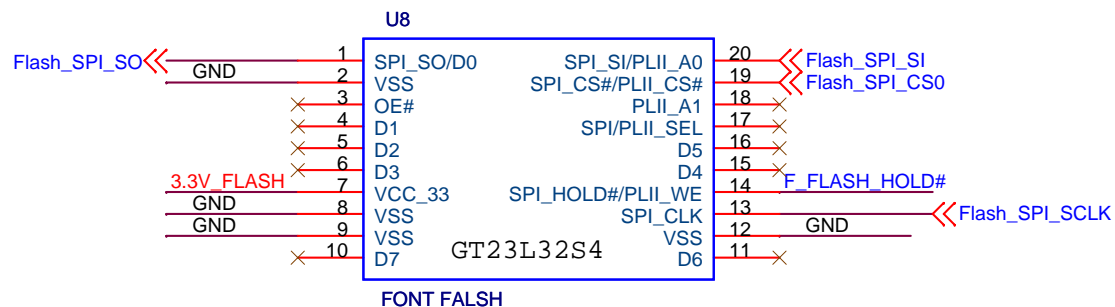
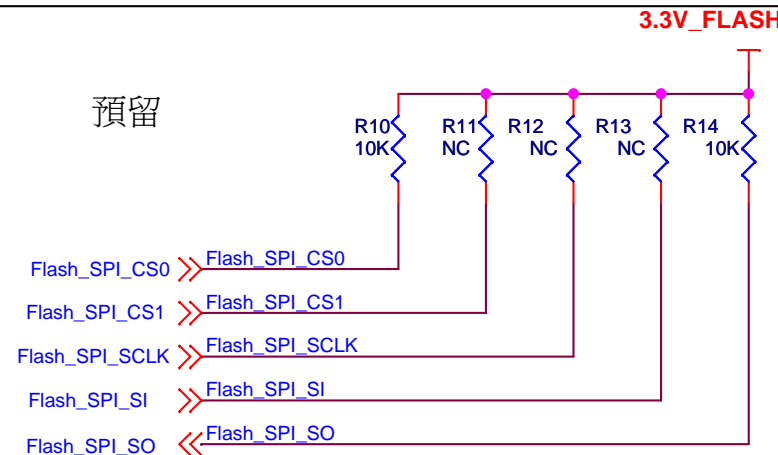
Rev
A

Date: Thursday, August 29, 2013

Sheet 3 of 10

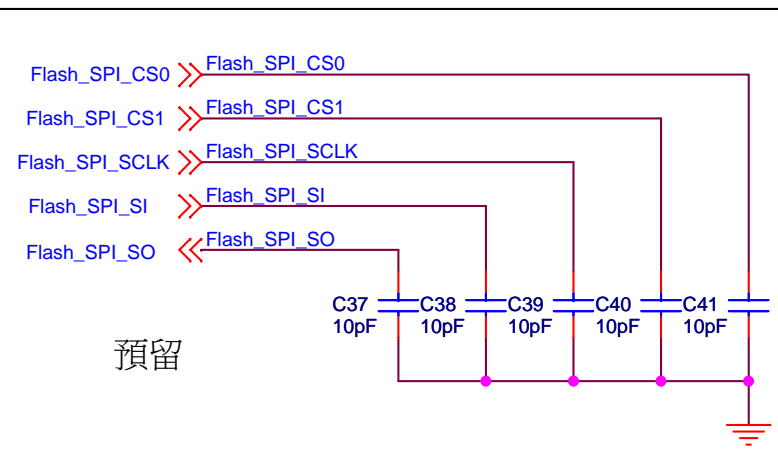


同位置 共同 LAYOUT



U9 PAD 加長

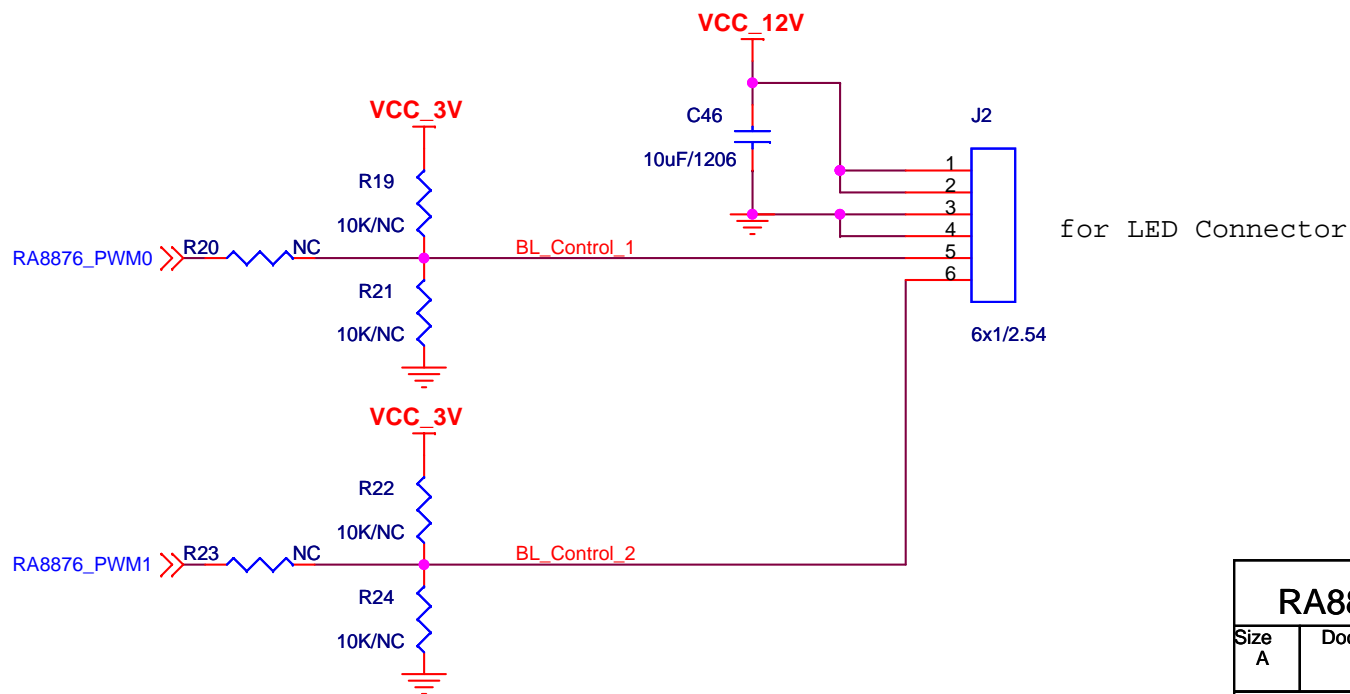
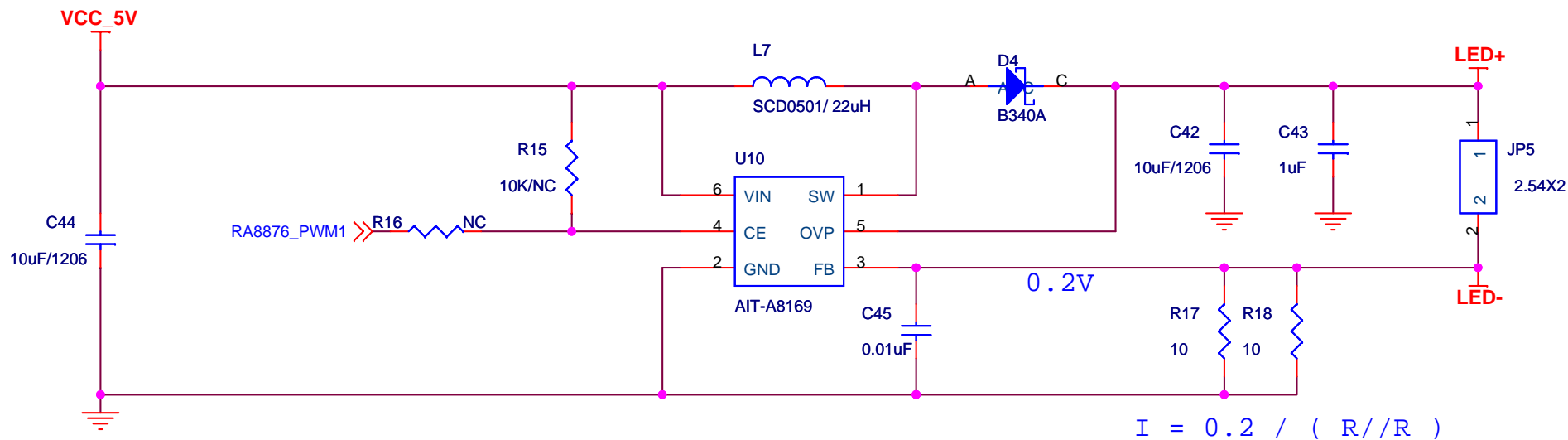
同位置 共同 LAYOUT



RA8876_B_Demo Board

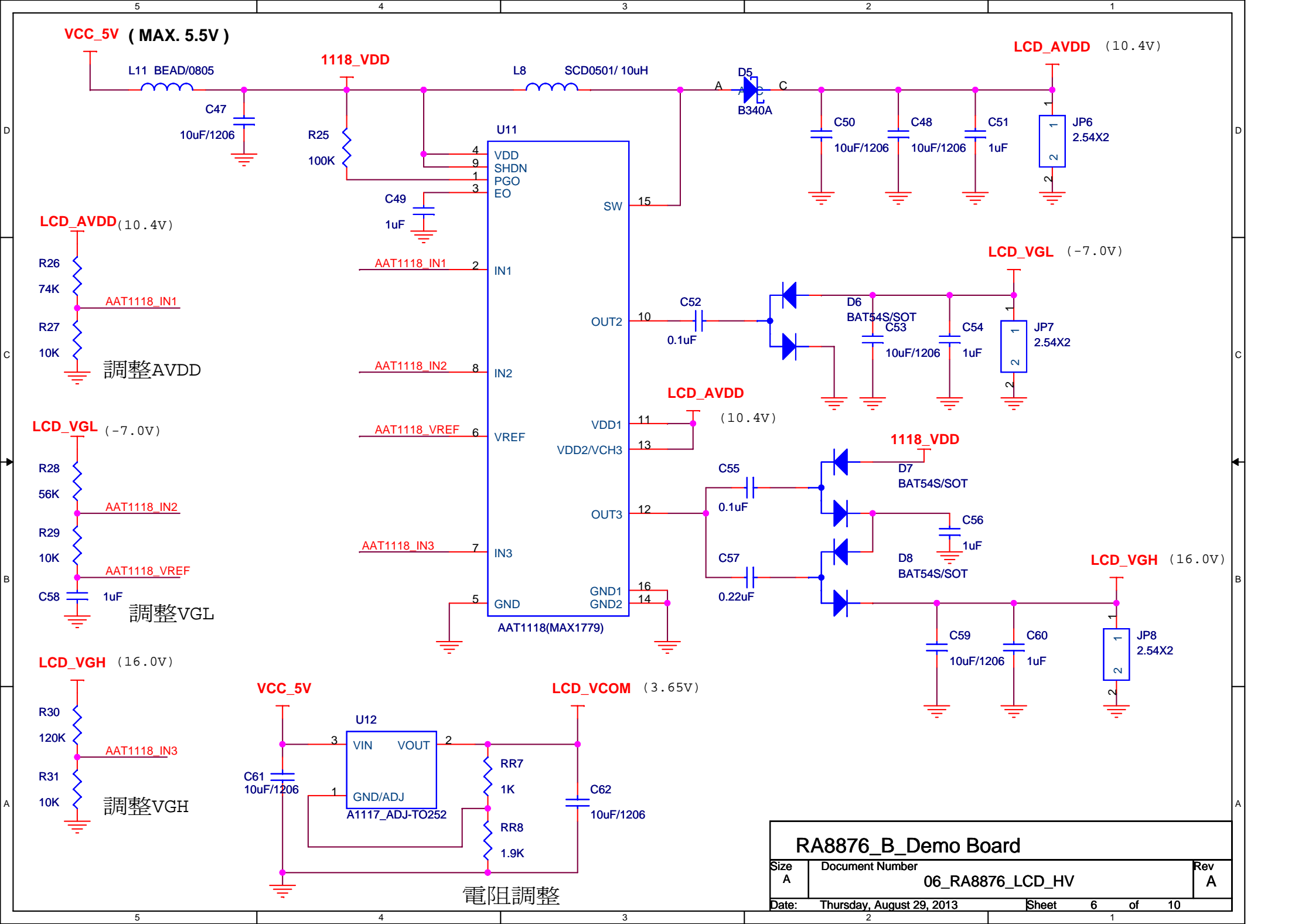
Size A	Document Number	Rev A
	04_RA8876_Flash	
Date:	Monday, September 02, 2013	Sheet 4 of 10

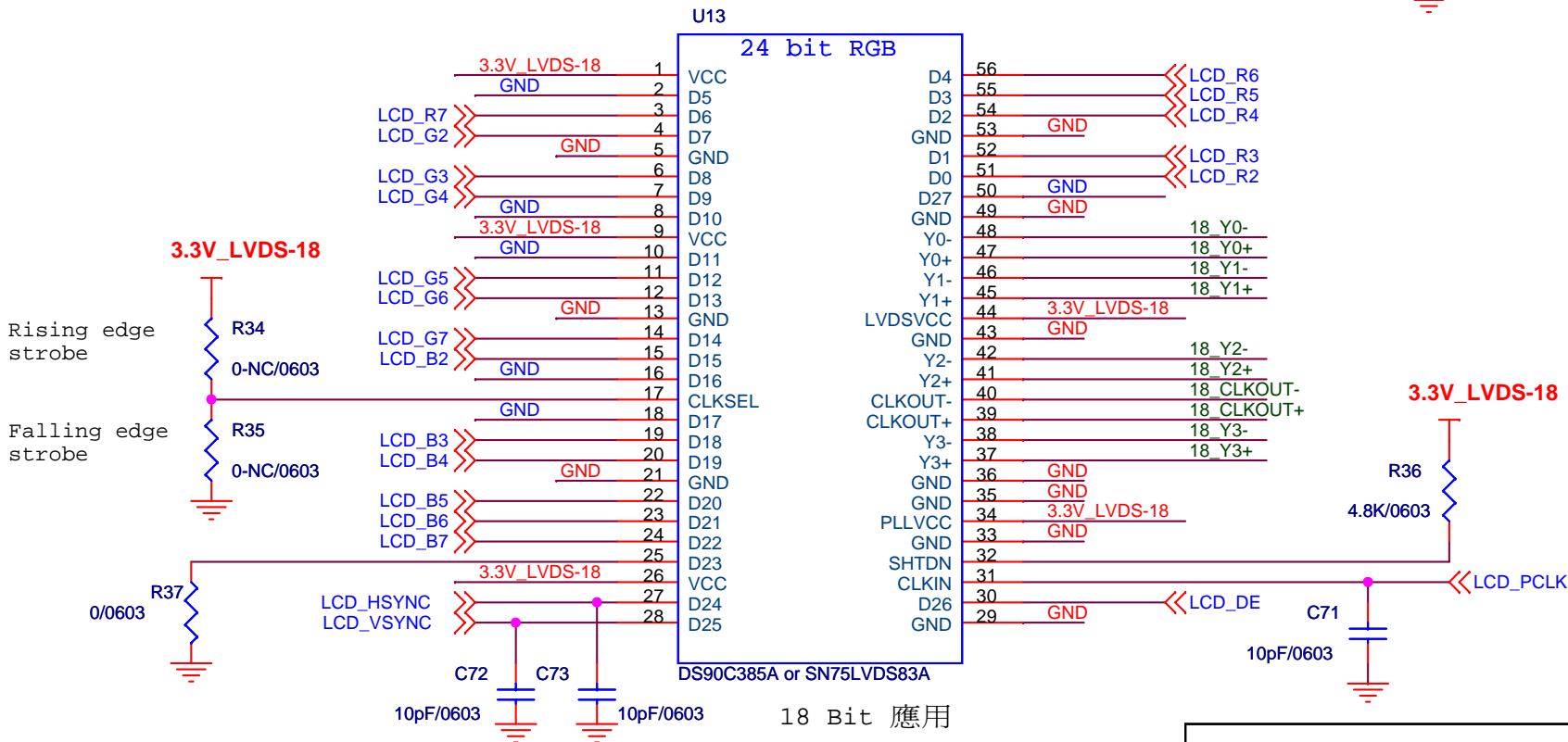
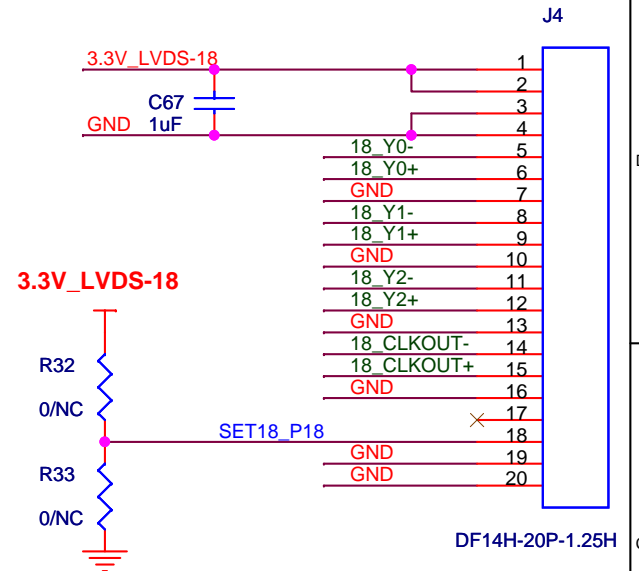
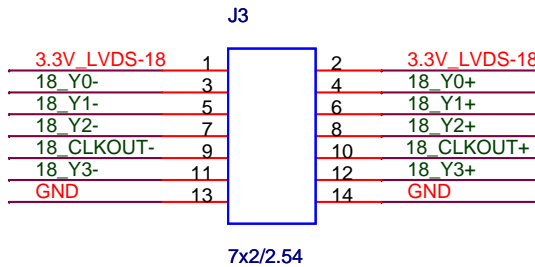
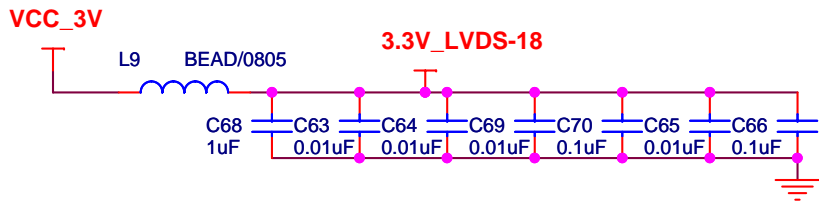
(MAX. 6.0V)



RA8876_B_Demo Board

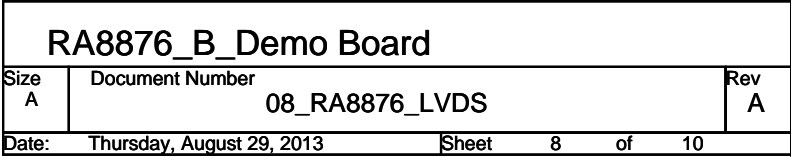
Size A	Document Number 05_RA8876_LCD_BL	Rev A
Date:	Thursday, August 29, 2013	Sheet 5 of 10

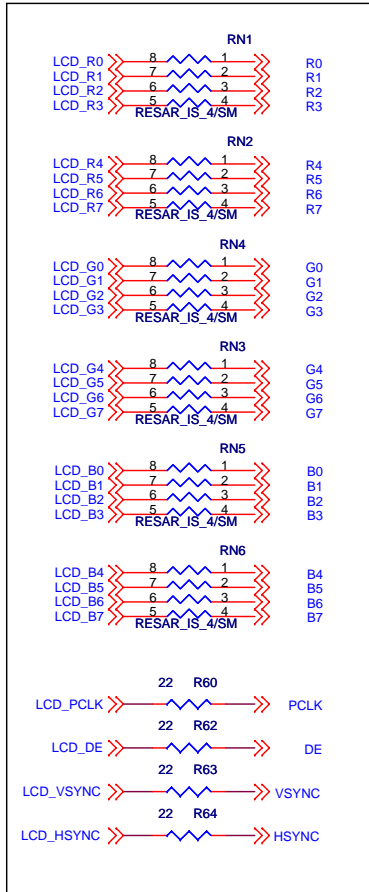




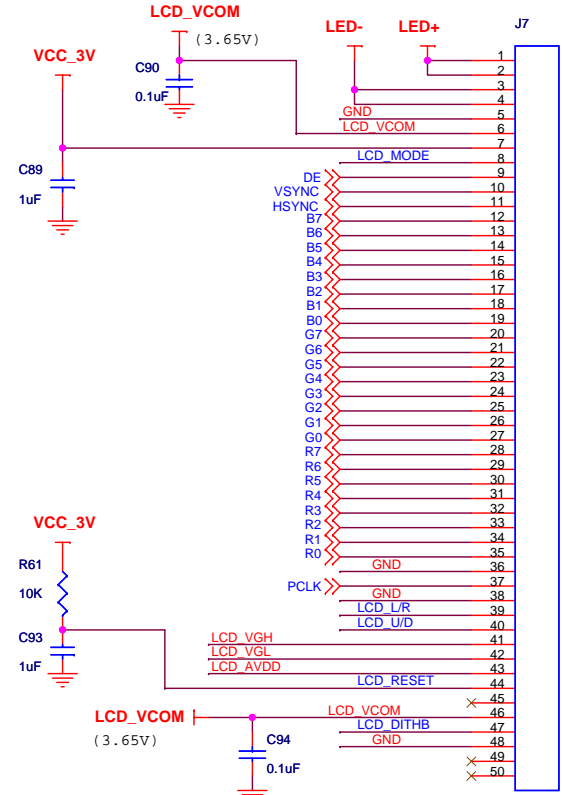
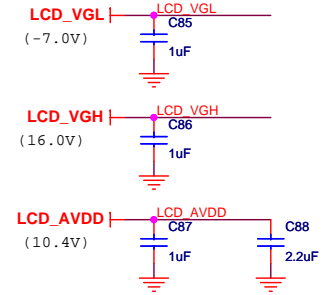
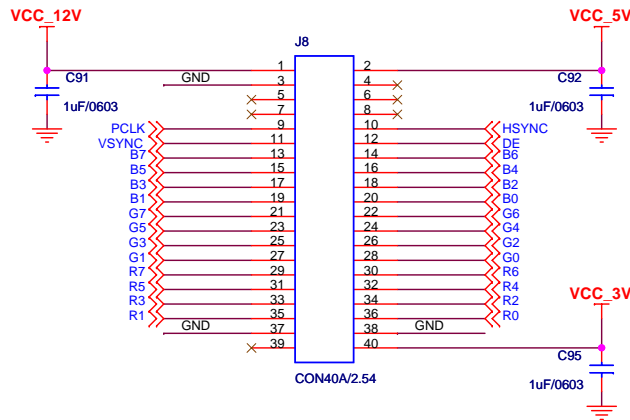
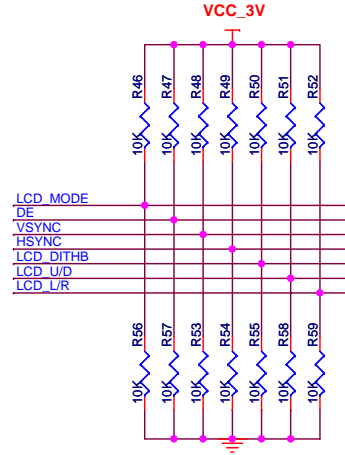
RA8876_B_Demo Board

Size	Document Number	Rev
A	07_RA8876_LVDS_6b	A
Date:	Thursday, August 29, 2013	Sheet 7 of 10





[AT070TN92]
 DE mode : mode=1
 Vsync=1
 Hsync=1
 SYNC mode : mode=0
 DE=0



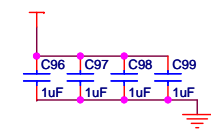
上接式 FPC: 0.5mm(0.35mm+0.15mm)

LCD: 群創_AT080TN52
 LED 10V

RA8876_B_Demo Board

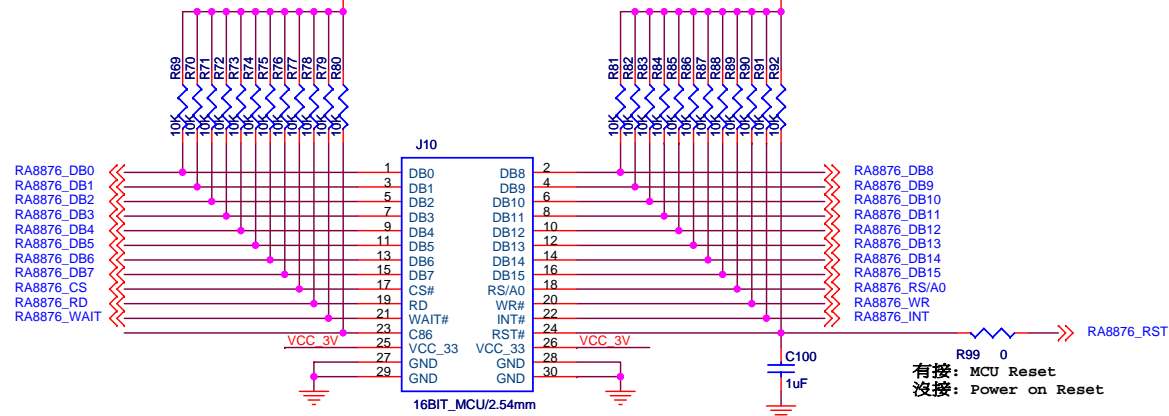
Size	Document Number	Rev
B	09_RA8876_LCD_I/F	A
Date:	Thursday, August 29, 2013	Sheet 9 of 10

VCC_3V

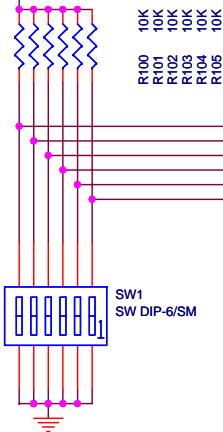


VCC_3V

VCC_3V



VCC_3V

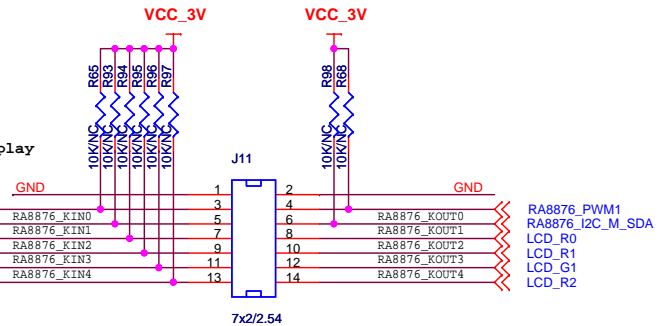


[P.6/5/4]
00x : 8080
01x : 6800
100 : 3-wire
101 : 4-wire
11x : I2C

RA8876_PS2
RA8876_PS1
RA8876_PS0
RA8876_TEST[2]
RA8876_TEST[1]
RA8876_TEST[0]

RA8876_PWM0=1:
enable Initial Display

RA8876_PWM0
RA8876_I2C_M_SCL
LCD_B0
LCD_B1
LCD_G0
LCD_B2



RA8876_B_Demo Board

Size	Document Number	Rev
B	10_RA8876_MCU_I/F	A
Date:	Thursday, August 29, 2013	Sheet 10 of 10